
Hardware Infrastructure Maintenance

These messages indicate errors within the hardware infrastructure. These maintenance messages relate to:

- Control Processor (CP) or Changeover and Memory Block (CMB)
- Switch Over (SWO)
- Network Control Bus (NCB)
- Connectors (CON)

The message output format is: Fields x, y, z

Where: x = side, y = card and z = port

HWI messages

HWI0001 x	HI Init: Cold start begins on side x.
HWI0002 x	HI Init: Warm start begins on side x.
HWI0003 x	HI Init: Graceful switchover start continues on side x.
HWI0004 x y	HI Init: Phase x (y) begins.
HWI0005 x y	HI Init: Cold start complete on side x, in y seconds.
HWI0006 x y	HI Init: Warm start complete on side x, in y seconds.
HWI0007 x y	HI Init: Switchover start complete on side x, in y seconds.
HWI0008 x	HI FS: Created data directory x.
HWI0009 x	HI FS: Saving data to directory x.

HWI0010 x	HI Init: Forced switchover start begin on side x.
HWI0013 x	NCB x y: Unmasking x interrupt.
HWI0101 x	CON x y: Connected to x.
HWI0102 x	CON x y: Disconnected from x.
HWI0103	CON x y: Connected device is unknown.
HWI0104	CON x y: Disconnected device is unknown.
HWI0105	CON x y: Connect event. Identification starts.
HWI0106 x	CON x y: Connect event. Creating x.
HWI0107 x y	CON x y: State transition from x to y.
HWI0108 x	CON x y: Created x.
HWI0109 x	CON x y: Simulating Disconnect-Connect for x.
HWI0110 x	CON x y: x deleted.
HWI0111 x	CON x y: Connect event. Notifying x.
HWI0112 x	CON x y: Disconnect event. Notifying x.
HWI0113 x	CON x y: Specification of new device x.
HWI0114 x y z	CON x y: x destroyed. Affected objects: y z.
HWI0115 x	CON x y: x destroyed.
HWI0116 x	CON x y: Successful probe of x.
HWI0117 x	CON x y: Failed to probe x.
HWI0118 x y	CON x y: Unconfigured x connected to y. Action: Verify that the hardware is installed.
HWI0119 x	CON x y: Unknown device connected to x. Action: Check card ID for board on core shelf or 3PE switch settings.
HWI0201 x	NCB x y: Enabling 3PE to support x.

HWI0202 x	NCB x y: Failed to enable 3PE on group x. Action: Check that 3PE has correct switch settings. Check both connector ends on the CNI to 3PE. Be sure the 3PE Enable/Disable switch is Enabled (up).
HWI0203 y	NCB x y: Disabling 3PE on group y.
HWI0204 x	NCB x y: Failed to disable 3PE on group x. Action: Check that 3PE has correct switch settings. Check both connector ends on the CNI to 3PE. Be sure the 3PE Enable/Disable switch is Enabled (up).
HWI0205	NCB x y: OOS testing 3PE.
HWI0206 x	NCB x y: No response from 3PE on group x. Action: Check that the CPU Norm/Maint switch is in Maint (down). Check both connector ends of the CNI to 3PE cable. Be sure the 3PE Enable/Disable switch is Enabled (up), and the 3PE switch settings are correct.
HWI0207	NCB x y: Unable to control 3PE state.
HWI0208	NCB x y: Failed OOS test.
HWI0209	NCB x y: Cannot read 3PE state.
HWI0210 x y	NCB x y: Failed to service interrupt x. Number of ISR serviced: n.
HWI0211 x y	Resuming service of interrupt x. Number of ISR service failures: n.
HWI0212 x y	NCB = masking interrupt. Action: Refer to STAT CNI in LD135 for recovery actions.
HWI0214	NCB x y: Unmasking all interrupts.
HWI0215 x y	NCB x y: Failed to unmask x, line d interrupt.
HWI0216 x	NCB x y: I/O interrupts from group x disabled. Action: Remove all unconfigured I/O cards from the group. Reseat or replace the packs. Reseat or replace the corresponding CNI board.
HWI0217 x y	NCB x y: x interrupts from group y disabled. Action: Reseat or replace card(s) for the group, and corresponding CNI card.
HWI0218 x y z, a b c	NCB x y: CNI to 3PE connection mismatch. Expected: x to 3PE y z . Detected: a to 3PE b c .

Action: Check connection and 3PE DIP switch setting.

HWI0219 y z NCB x y: masking interrupt "y" failed to detect source of this interrupt z times.

HWI0301 CP: CP to CP cable (top) is faulty or has been removed.

Action: Check both connector ends of the cable.

HWI0302 CP: CP remote power failed. Check CEPS on the other core shelf.

Action: Be sure both ends of the CP to CP cable are connected

HWI0303 CP remote power restored.

HWI0304 CP to CP cable (top) installed.

HWI0305 Number of SIMMs found is less than configured in the database.

Action: Reseat SIMM on CP card.

HWI0306 x y SIMM x y: memory fault will impact real time capacity.

Action: The faulty CP card may need to be replaced.

HWI0320 x SIMM x y: From side x this SIMM appears without base address.

HWI0321 x SIMM x y: From side x this SIMM appears without size attributes.

HWI0322 n SIMM x y: From side x this SIMM appears with unknown size: n.

HWI0323 y z SIMM x: Total DRAM on side 0 : y Megabytes side 1: z megabytes. Memory mismatch may affect switch over decision.

HWI0324 x y z SIMM x: Total equipped DRAM on CP x : y Megabytes . Specified amount of DRAM: z. Check SIMMs on CP side x.

HWI0325 n x SIMM x: SIMMs on Core side n: x x x x x x . Where: x = SIMM size

HWI0326 x SIMM x: The SIMMs on Core side x are not accessible.

Action: Check the connector ends of the CP to CP cable.

HWI0401 SWO%d: Switchover denied. System is in maintenance mode.

Action: Ensure both CP Maint/Norm switches are in Norm mode (up).

HWI0402 SWO x: Switchover denied. Redundancy not available.

Action: Check both connector ends of the CP to CP cable.

HWI0403	SWO x: Switchover attempt unsuccessful.
HWI0404	SWO x: Switchover denied. Other side deemed not better.
HWI0405	SWO x: Switchover denied. Critical resource is locked.
HWI0406	SWO x: Failure occurred attempting to suspend SL-1 task.
HWI0407	SWO x: Failure occurred attempting to resume SL-1 task.
HWI0408	SWO x: Attempted to switchover when memory not synchronized.
HWI0409 x y	SWO x: Switch back to side x failed. Will un-suspend SL-1 on side y.
HWI0412	SWO %s: Switchover requested, other side deemed better. (%d will be replaced with Core Shelf number. Action: No action to be taken by craftsperson.
HWI0500	CP: Remote ready deasserted.
HWI0501	CP: Remote ready asserted.
HWI0502	CP: Remote power bit deasserted.
HWI0503	CP: Secondary CP not accessible. Action: Check the CP to CP cable.
HWI0504	CP: Cable time out.
HWI0505	CP: Primary parameter error.
HWI0506	CP: Secondary parameter error.
HWI0507	CP: Write without unlock.
HWI0525	CP: Spurious interrupt.
HWI0526	CP: RSC = 0 x%08x (Task level)
HWI0527	CP: RSC = 0 x%08x (Interrupt level)
HWI0528	CP: Standby CP asserts READY. Begin Shadowing.
HWI0529	CP: Primary CP cannot access secondary CP. Action: Check the cable.

HWI0530	CP: System split by Maint/Norm switches.
HWI0531	CP: MAINT/MAINT to MAINT/NORM does not shadow.
HWI0532	CP: Memory synchronization failed.
HWI0533	CP: Memory synchronization completed.
HWI0534 x	CP: Memory synchronization begun. CP x is master.
HWI0535 x y	CP: Fatal error occurred on CP x. Processing will continue on CP y.
HWI0536	CMB 0: Flash ROM mismatch -- staying in Single mode. Both sides must have the same released installed.